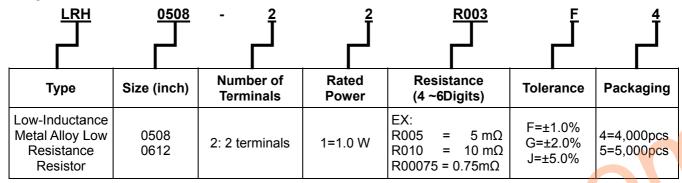
LRH Series Wide Terminal Metal Alloy Low-Resistance Resistor Product Specifications

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1 Scope:

- 1.1 This specification is applicable to lead free and halogen free of RoHS directive for LRH series wide terminal metal alloy low-resistance resistor
- 1.2 The product is for general electronic purpose.

2 Explanation Of Part Numbers:



3 Product Specifications:

Туре	# of Terminals	Max. Rating Power	Max. Rating Current	Max. Overload Current	T.C.R. (ppm/°C)	Industance	Resistance Range (mΩ) F (±1%) G (±2%) J (±5%)	Operating Temperature Range
0508	2	1.0W	22.36A	44.72A	≦±50	< 5nH	2~14	
0612	2	1.0W	31.6A	63.2A	≦±100	< 5nH	1≦R<5	-55~+150°C
0012	2	1.000	31.0A	03.2A	≦±50	\ 011H	5≦R≦25	

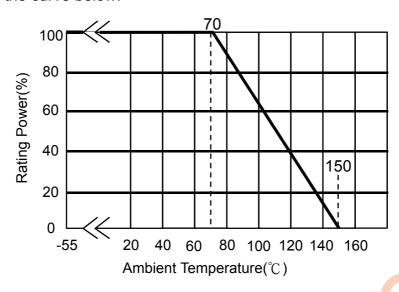
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3.1 Power Derating Curve: Operating Temperature Range: - 55 ~+150 $^{\circ}\mathrm{C}$

For resistors operated in ambient temperatures 70°C, power rating shall be derated in accordance with the curve below:



3.2 Rating Current:

Rated Current: The resistor shall have a DC continuous working current or a RMS(Root Mean Square). AC continuous working current at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

Remark:



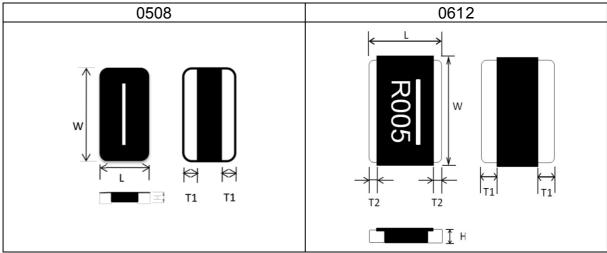
I=Rating Current(A)
P= Rating Power(W)
R=Resistance(Ω)

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4 Physical Dimensions:



Time	Maximum Power	Resistance		Dimensio	ns - in inches	(millimeters)	
Туре	Rating (Watts)	Range (mΩ)	L	W	н	T1	T2
0508	1	2~14	0.05±0.008 (1.270±0.20)	0.05±0.008 (2.032±0.20)	0.012±0.004 (0.30±0.10)	0.014±0.006 (0.35±0.15)	
0612	1	1~16	0.059±0.008	0.126±0.008	0.010±0.004	0.012±0.006 (0.30±0.15)	0.008±0.006
0012	, '	17~25	(1.50±0.20)	(3.20±0.20)	(0.25±0.10)	0.010±0.006 (0.25±0.15)	(0.20±0.15)

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5 Reliability Performance:

5.1 Electrical Performance:

Test Item	Conditions of Test					Test Limits
Temperature	• TCR (ppm/°C) =(R2-R1) R1 (T2-T1) X 10 ⁶					Refer to Paragraph 3. general specifications
	• R1	: resistance	of room temp			
		: resistance				
\ - /		: Room temp				
		•	re at 150 °C			
		fer to JIS C		- and ralages the la	ad for	0500. < 10.50/
	Applied Overload for 5 seconds and relea					0508: ≤±0.5%
	about 30 minutes, then measure its resistance variance rate. (Overload condition refer to below):					***************************************
Short Time	(Overi]	No evidence of mechanical damage
Overload	-	Type 0508	Power (W) 1.0	# of rated power 4 times		
	-	0612	1.0			
	Defer	to JIS C 520		4 times		
				44 400 \/DO :		
			,	dd 100 VDC in + ,- i		
Insulation	for 60 secs then measured the insulation resistance between					$\geq 10^8 \Omega$
Resistance	electrodes and insulating enclosure or between electrodes					$\leq 10 \Omega$
	and base material. Refer to JIS-C5201-1 4.6					
	Applied 300VAC for 1 minute, and Limit surge current 50					
	mA (max.)					No short or burned on the appearance.
	,	to JIS-C520	1-1 4 7			the chart of burned on the appearance.

5.2 Mechanical /Constructional Performance:

Test Item	Conditions of Test	Test Limits						
Resistance to Solder Heat	The tested resistor be immersed 25 mm/sec into molten solder of 260±5°C for 10±1secs. Then the resistor is left in the room for 1 hour, and measured its resistance variance rate. Refer to JIS-C5201-1 4.18	≤±0.5% No evidence of mechanical damage						
Solderability	Add flux into tested resistors, immersion into solder bath in temperature 245±5°C for 3±0.5secs. Refer to JIS-C5201-1 4.17	Solder coverage over 95%						
Vibration	The resistor shall be mounted by its terminal leads to the supporting terminals on the solid table. The entire frequency range :from 10 Hz to 55 Hz and return to 10 Hz, shall be transferred in 1 min. Amplitude : 1.5mm This motion shall be applied for a period of 4 hours in each 3 mutually perpendicular directions (a total of 12hrs) Refer to JIS-C5201-1 4.22	-						
Resistance to solvent	The tested resistor be immersed into isopropyl alcohol of $20{\sim}25^{\circ}{\circ}$ for 60secs, then the resistor is left in the room for 48 hrs. Refer to JIS-C5201-1 4.29	≤±0.5% No evidence of mechanical damage						

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5.3 Environmental Performance:

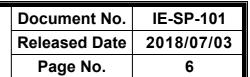
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Test Item	Conditions of Test	Test Limits
Low Temperature	Put the tested resistor in chamber under temperature -55±2 °C for 1,000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.23.4	≤±0.5% No evidence of mechanical damage
High Temperature Exposure	Put tested resistor in chamber under temperature 150±5°C for 1,000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.23.2	≦±1.0% No evidence of mechanical damage
Temperature Cycling (Rapid Temperature Change)	Put the tested resistor in the chamber under the temperature cycling which shown in the following table shall be repeated 1,000 times consecutively. Then leaving the tested resistor in the room temperature for 60 minutes, and measure its resistance variance rate. Testing Condition Lowest Temperature -55 +0/-10°C Highest Temperature 150 +10/-0°C Refer to JIS-C5201-1 4.19	No evidence of mechanical damage
Moisture Resistance (Climatic	Put the tested resistor in chamber and subject to 10 cycles of damp heat and without power. Each one of which consists of the steps 1 to 7 (Figure 1). Then leaving the tested resistor in room temperature for 24 hr, and measure its resistance variance rate. Refer to MIL-STD 202 Method 106	≤±0.5% No evidence of mechanical damage
Riae Humidity	Put the tested resistor in chamber under 85± 5°C and 85± 5%RH with 10% bias and load the rated voltage for 90 minutes on, 30 minutes off, total 1,000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.24	≤±1.0% No evidence of mechanical damage

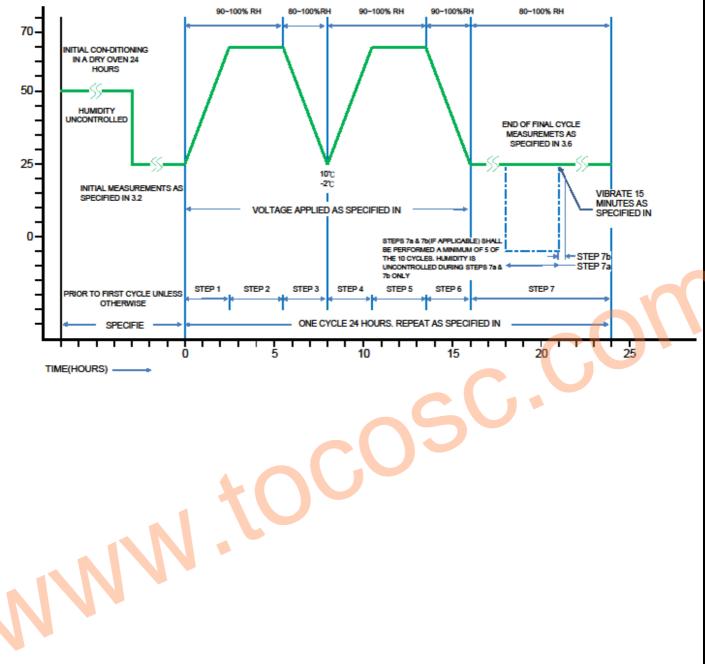
5.4 Operational Life Endurance:

Test Item	Conditions of Test	Test Limits
MA	Put the tested resistor in chamber under temperature 70±	≦±1.0%
Load Life	2°C and load the rated voltage for 90 minutes on 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.25	No evidence of mechanical damage

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6 Marking Format:

- 6.1 Product resistance is indicated by using two marking notation styles:
 - a. "R" designates the decimal location in ohms, e.g.
 - For $1m\Omega$ the product marking is R001;
 - For 25mΩ the product marking is R025;
 - For $100 \text{m}\Omega$ the product marking is R100.
 - b. "m" designates the decimal location in milliohms, e.g.
 - For $0.25m\Omega$ the product marking is 0m25;
 - For $0.5m\Omega$ the product marking is 0m50;
 - For $5.5m\Omega$ the product marking is 5m50;
 - For $25.5m\Omega$ the product marking is 25m5.



6.2 Marking Styles by Laser:

0508	0612

Marking Type	R	m	1	2	3	4	5	6	7	8	9	0
0612					(T)				7			

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7 Plating Thickness:

7.1 Ni : \geq 2 μ m

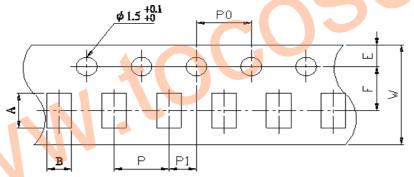
7.2 Sn(Tin) : $\ge 3 \mu$ m 7.3 Sn(Tin) : Matte Sn

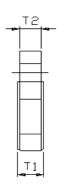
8 Measurement Point:

Bottom electrode			Unit : mm
	DIM TYPE	А	В
A	0508	0.90±0.05	0.46±0.05
 Current Terminal Voltage Terminal 	0612	1.20±0.05	0.46±0.05

9 Taping specifications:

9.1 Tape Dimensions:





DIRECTION OF FEED

CARRIER TAPE

Unit: mm

lt	DIM em	Α	В	W	E	F	T1	T2	Р	P0	10*P0	P1
	0508	2.30±0.10	1.55±0.10	8.0±0.20	1.75±0.10	3.5±0.05	0.40+0.2/-0	0.40±0.10	4.0±0.10	4.0±0.10	40.0±0.20	2.0±0.05
	0612	3.50±0.20	1.90±0.20	8.0±0.20	1.75±0.10	3.5±0.05	0.60+0.2/-0	0.60±0.05	4.0±0.10	4.0±0.10	40.0±0.20	2.0±0.05

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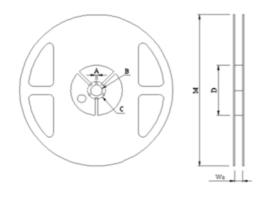
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9.2 Packaging model:

	T	Max. Packaging Quantity (pcs/reel)
Туре	Tape width	Carrier Tape
	Width	4mm pitch
0508	8mm	5,000pcs
0612	8mm	5,000pcs

9.3 Reel Dimensions:



Unit: mm

Reel Type / Tape	w	M	Α	В	C	D
7" reel for 8 mm tape	12.00±0.5	178±1.0	2.0±0.5	13 <mark>.2</mark> ±0.5	17.7±0.5	60.0±1.0

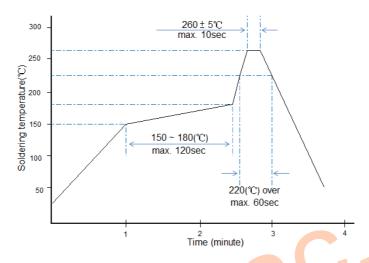
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10 Technical application notes: (This is for recommendation, please customer perform adjustment according to actual application)

- 10.1 Recommend soldering method:
 - 10.1.1 This product is applicable to IR-reflow process only.(Infrared Reflow)
 - 10.1.2 Typical examples of soldering processes that provides reliable joints without any damage are given in below:



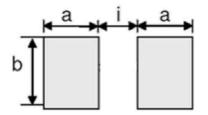
Recommended IR Reflow Soldering Profile

MEET J-STD-020D

10.1.3 Soldering Iron: temperature 350°C±10°C , dwell time shall be less than 3 sec.

10.2 Recommend Land Pattern:

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



Туре	Maximum Power	Resistance	Dimensions - in millimeters		eters
.,,,,	Rating (Watts)	Range (mΩ)	а	b	i
0508	1.00	2~14	0.45	2.20	0.55
0612	1.00	1~25	1.65	3.50	0.5

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10.3 The characteristic of Fe/Cr/Al alloy material:

Because of including magnetism, inductor will be generated under high frequency circuit then to cause value shift and influence customer application. If there is related application shall be noted especially or discuss with original factory.

10.4 Environment Precautions:

This specification product is for general electronic use, RALEC will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications need to confirm with RALEC.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as Cl2 \ H2S \ NH3 \ SO2 and NO2.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product. After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

10.5 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

10.6 Operation and Processing Precautions:

- (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
- (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.
- (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
- (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

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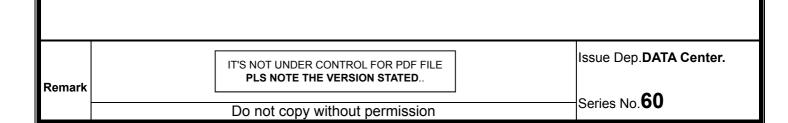
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11 Storage and transportation requirement:

- 11.1 The temperature condition must be controlled at 25±5°C, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years ∘
- 11.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as CI2 \ H2S \ NH3 \ SO2 and NO2.
- 11.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

12 Attachments

12.1 Document Revise Record (QA-QR-027)



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